

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Continuation of Seok-Jun Won

Application Serial No. TBA

Filed: Concurrently herewith

For: **INTEGRATED CIRCUIT CAPACITORS HAVING A DIELECTRIC  
LAYER BETWEEN A U-SHAPED LOWER ELECTRODE  
AND A SUPPORT LAYER**

Date: September 17, 2003

Mail Stop PATENT APPLICATION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Sir:

Attached is a list of documents on form PTO-1449. Items 1-7 listed on the PTO-1449 were cited in parent application Serial No. 10/028,187, filed December 20, 2001. Since the benefit of this application is claimed under 35 U.S.C. §120, no copies need to be furnished in accordance with 37 C.F.R. §1.98(d); however, copies will be furnished on request. It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. §1.56 and Section 609 of the MPEP.

No fee is believed due. However, the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

Respectfully submitted,



Elizabeth A. Stanek

Registration No. 48,568

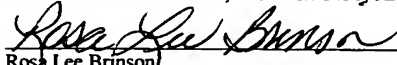
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**CERTIFICATE OF EXPRESS MAILING**

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Date of Deposit: September 17, 2003

I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR § 1.10 on the date indicated above and is addressed to: Mail Stop PATENT APPLICATION, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

  
Rosa Lee Brinson

**Examiner:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.